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22141 U.S. PTC 10/680665

Docket No.: SNDK.294US0

October 6, 2003

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Enclosed herewith for filing is a patent application, as follows:

Inventor(s):

G. Matamis et al.

Title:

Bitline Direction Shielding to Avoid Cross Coupling Between Adjacent Cells for Nand

Flash Memory

- X Check in the amount of \$1026.00
- X Return Receipt Postcard
- X This Transmittal Letter (in duplicate)
- 9 page(s) Specification (not including claims)
- 4 page(s) Claims
- 1 page Abstract
- 5 Sheet(s) of Drawings
- 4 page(s) Declaration For Patent Application and Power of Attorney
- 2 page(s) Power of Attorney By Assignee of Entire Interest

CLAIMS AS FILED

For Total Claims	Number <u>Filed</u> 26	-20	=	Number <u>Extra</u> 6	x	<u>Rate</u> \$18.00	=	\$ \$	Basic Fee <u>750.00</u> 108.00
Independent Claims	5	-3	=	2	х	\$84.00	=	\$	168.00
Fee of for the first filing of one or more multiple dependent claims per application								\$,
Total fee for filing the patent application in the amount of								\$	<u>1026.00</u>
The Commis	sioner is here credit any ove						hich may	be	

EXPRESS MAIL LABEL NO:

EV 321715981 US

Respectfully submitted,

Peter A. Gallagher Attorney for Applicant(s)

Reg. No. 47,584